



Product/Process Change Notice - PCN 14_0200 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

PCN Title: ADUM1400/01/02 Die Revision

Publication Date: 10-Oct-2014

Effectivity Date: 08-Jan-2015 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change

Design Changes:

1. Increased pulse width of disable signal for refresh block. Increased separation between falling edge of disable signal for refresh block and first refresh high pulse. Increased separation between consecutive pulses on rising edge and refresh high pulses.
2. Reduced the propagation delay time of receiver.

Reason For Change

1. Ensure the driver can reliably send out the refresh pulses. Ensure the receiver can reliably detect all edge pulses and refresh pulses.
2. Maintain the propagation delay time as original version.

Impact of the change (positive or negative) on fit, form, function & reliability

No change to fit, form, or reliability. Improved functionality.

Summary of Supporting Information

Qualification has been performed per ADI0012, Procedure for Qualification of New or Revised Processes. See attached Qualification Report Summary.

Supporting Documents

Attachment 1: Type: Qualification Results Summary

ADI_PCN_14_0200_Rev_-_ADuM140x Die Rev.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas: PCN_Americas@analog.com

Europe: PCN_Europe@analog.com

Japan: PCN_Japan@analog.com

Rest of Asia: PCN_ROA@analog.com

Appendix A - Affected ADI Models**Added Parts On This Revision - Product Family / Model Number (35)**

ADUM1400 / ADUM1400ARW	ADUM1400 / ADUM1400ARW-RL	ADUM1400 / ADUM1400ARWZ	ADUM1400 / ADUM1400ARWZ-RL	ADUM1400 / ADUM1400BRW
ADUM1400 / ADUM1400BRW-RL	ADUM1400 / ADUM1400BRWZ	ADUM1400 / ADUM1400BRWZ-RL	ADUM1400 / ADUM1400CRW	ADUM1400 / ADUM1400CRW-RL
ADUM1400 / ADUM1400CRWZ	ADUM1400 / ADUM1400CRWZ-RL	ADUM1401 / ADUM1401ARW	ADUM1401 / ADUM1401ARW-RL	ADUM1401 / ADUM1401ARWZ
ADUM1401 / ADUM1401ARWZ-RL	ADUM1401 / ADUM1401BRW	ADUM1401 / ADUM1401BRW-RL	ADUM1401 / ADUM1401BRWZ	ADUM1401 / ADUM1401BRWZ-RL
ADUM1401 / ADUM1401CRW	ADUM1401 / ADUM1401CRW-RL	ADUM1401 / ADUM1401CRWZ	ADUM1401 / ADUM1401CRWZ-RL	ADUM1402 / ADUM1402ARW
ADUM1402 / ADUM1402ARW-RL	ADUM1402 / ADUM1402ARWZ	ADUM1402 / ADUM1402ARWZ-RL	ADUM1402 / ADUM1402BRW	ADUM1402 / ADUM1402BRWZ
ADUM1402 / ADUM1402BRWZ-RL	ADUM1402 / ADUM1402CRW	ADUM1402 / ADUM1402CRW-RL	ADUM1402 / ADUM1402CRWZ	ADUM1402 / ADUM1402CRWZ-RL

Appendix B - Revision History			
Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	10-Oct-2014	08-Jan-2015	Initial Release

Analog Devices, Inc.

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